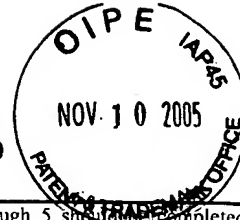


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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/668,252	09/24/2003	Hitoshi Eido	OKI.571	4296

TITLE OF INVENTION: TEST METHOD OF MEMORY IC FUNCTION ON DEVICE BOARD WITH DYNAMIC COMPETING CYCLE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/05/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
LAU, TUNG S	2863	702-117000

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Oki Electric Industry Co., Ltd.

Tokyo, Japan

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Andrew J. Telesz, Jr.Date 11-10-05Typed or printed name ANDREW J. TELESZ, JR.Registration No. 33,581

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(37 C.F.R. 1.346)

Docket No.

OKI.571

Applicant(s): Hitoshi Endo et al.

Application No.

10/668,252

Filing Date

September 24, 2003

Examiner

T. Lau

Customer No.

20987

Group Art Unit

2863

Confirmation No.

4296

Invention: TEST METHOD OF MEMORY IC FUNCTION ON DEVICE BOARD WITH DYNAMIC COMPETING CYCLE

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Dated: November 10, 2005

ANDREW J. TELESZ, JR.
REG. NO. 33,581

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